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What is UML?

- UML is an Object-Oriented modeling language.
- UML originates from OMT, Booch, and OOSE developed by Rumbaugh, Booch, and Jacobsen respectively.
- UML is maintained and further developed by the OMG.
- Telelogic is a full member and is also co-chairing the RTAD (real-time analysis design) group.
- We are defining the extensions (SDL) to UML that will include support for real-time modelling.

It is a notation, not a method!

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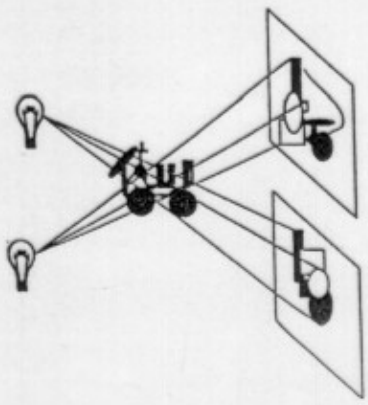
The notation

Most systems being modeled are so complex that a single kind of diagram cannot clearly and completely describe the system. Because of this, UML provides different diagram types, each type offering a different view or perspective on the system.



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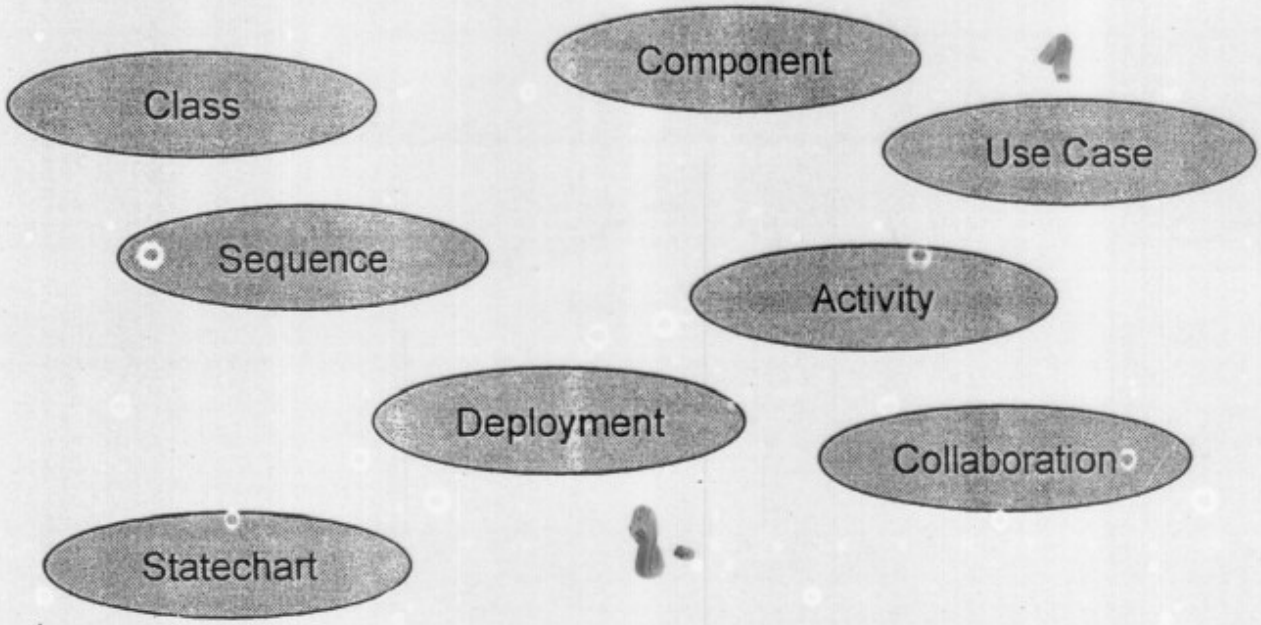
UML diagrams



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Building Requirements Model with Use Case Diagrams

- Purpose: discover, describe, agree to the functionality required of the system
- Main audience:
  - Users and management, who need to plan and fund the project
  - Analysts, who need to understand what is being requested
- Remember to exclude: the ***form*** of the system. This is about the ***function*** of the system
- Describes what an end user expects from the system

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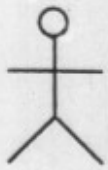
Actors

- A type of external entity that interacts with the system.
  - Users
  - Other systems or devices
- Initiates, communicates with, or receives information from one or more use cases



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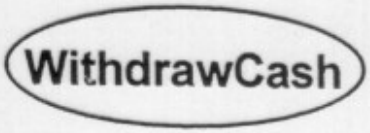
Use Case

- Named for a ***goal*** of an actor
- Initiated by an actor
- An abstraction of an actor/system interaction
- Represents possible interaction sequences
  - Well-defined starting point and goal
  - Includes sequences that fail to reach the goal



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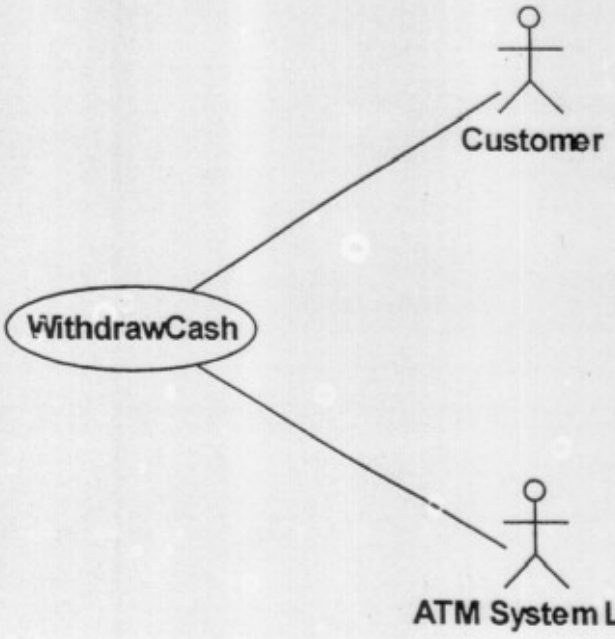
Use Case Association



- Drawn between actor and use case
- Represents the communication between actor and system
- Can be:
  - undirected - either end might initiate communication
  - directed - shows direction of flow of the initiating event

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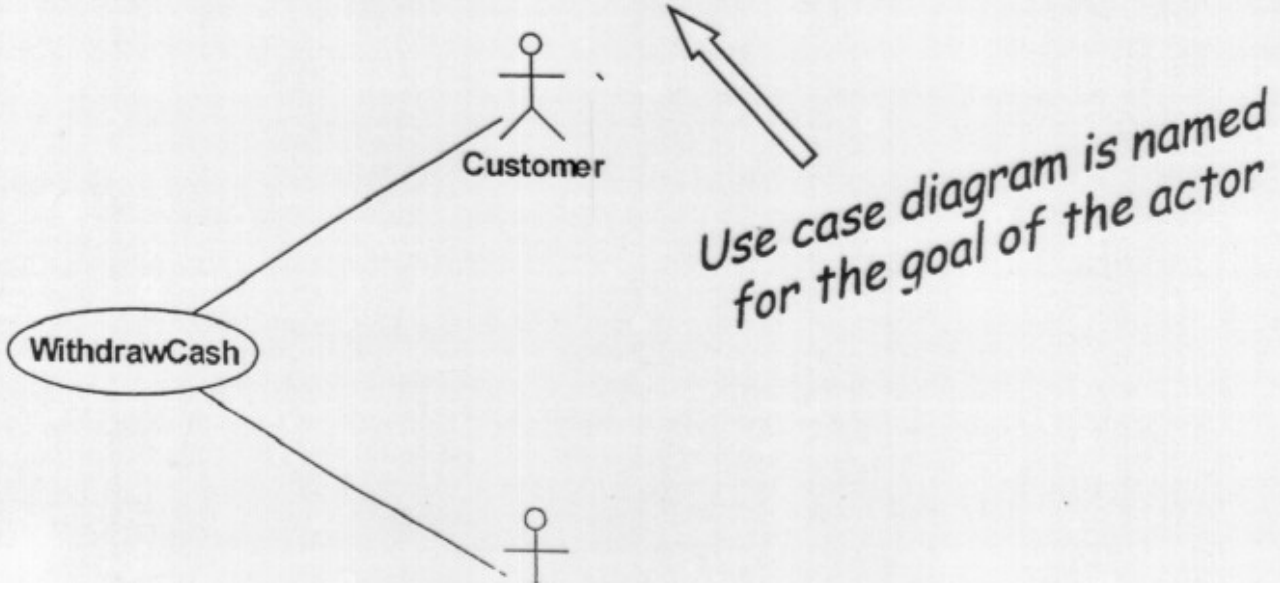
Use Case Diagram Example: ATM - Withdraw Cash



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Stereotypes

- It is possible to make extensions to the notation described in UML. The extensions are called stereotypes.
- A stereotype is written within guillemets, (« »). There are a number of predefined stereotypes, for example «actor», «uses», and «extends».
- Includes the properties of the base symbol

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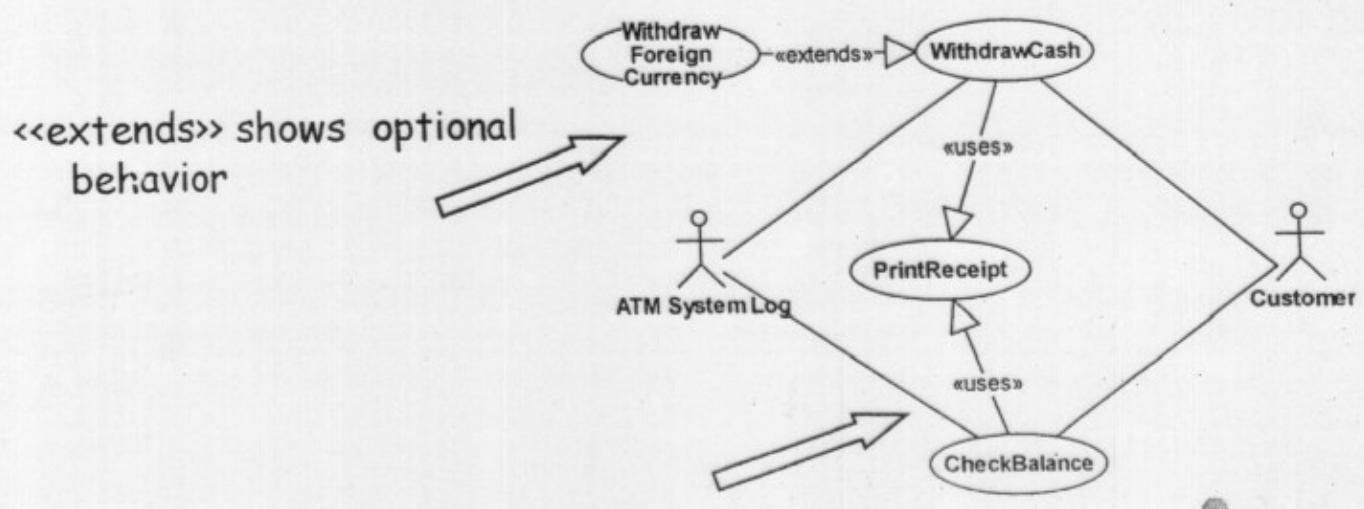
Use Case - «uses» and «extends».



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Modeling Object Interactions using Sequence Diagrams

- Purpose: draw scenarios based on basic and alternate courses of use cases.
- Typically one or more sequence diagrams per use case, starting with the basic course
- Shows messages between objects, which is the only way objects can interact

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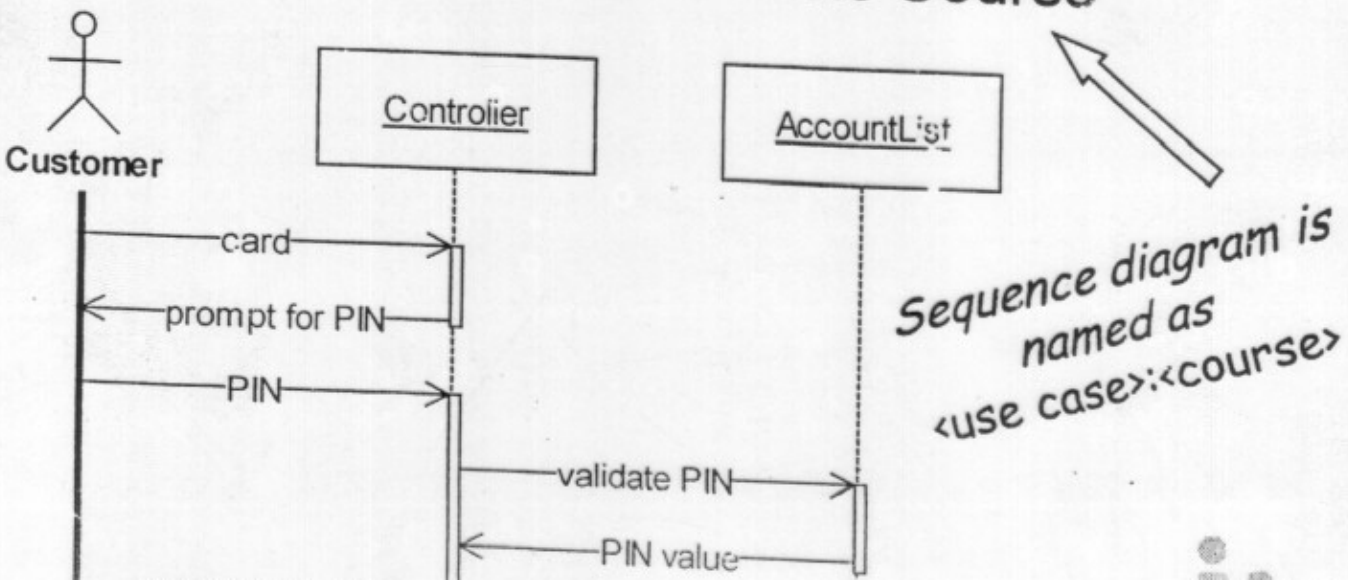
Sequence Diagram Example Withdraw Cash:Basic Course



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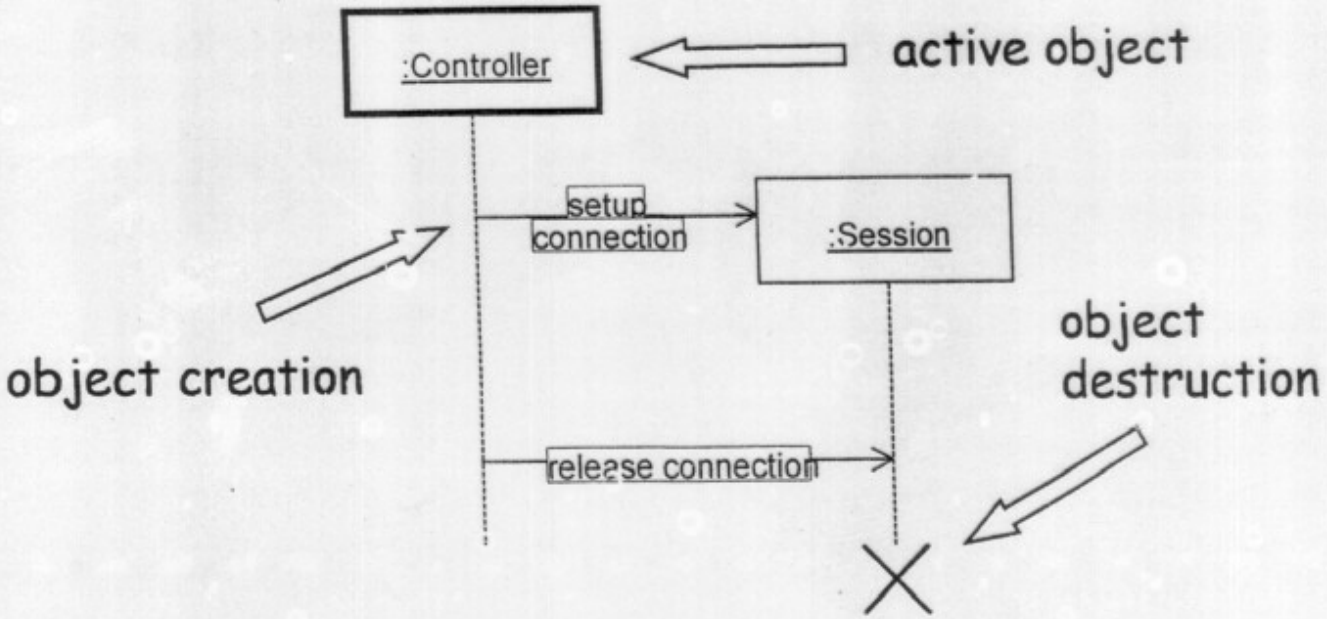
Sequence Diagram (continued)



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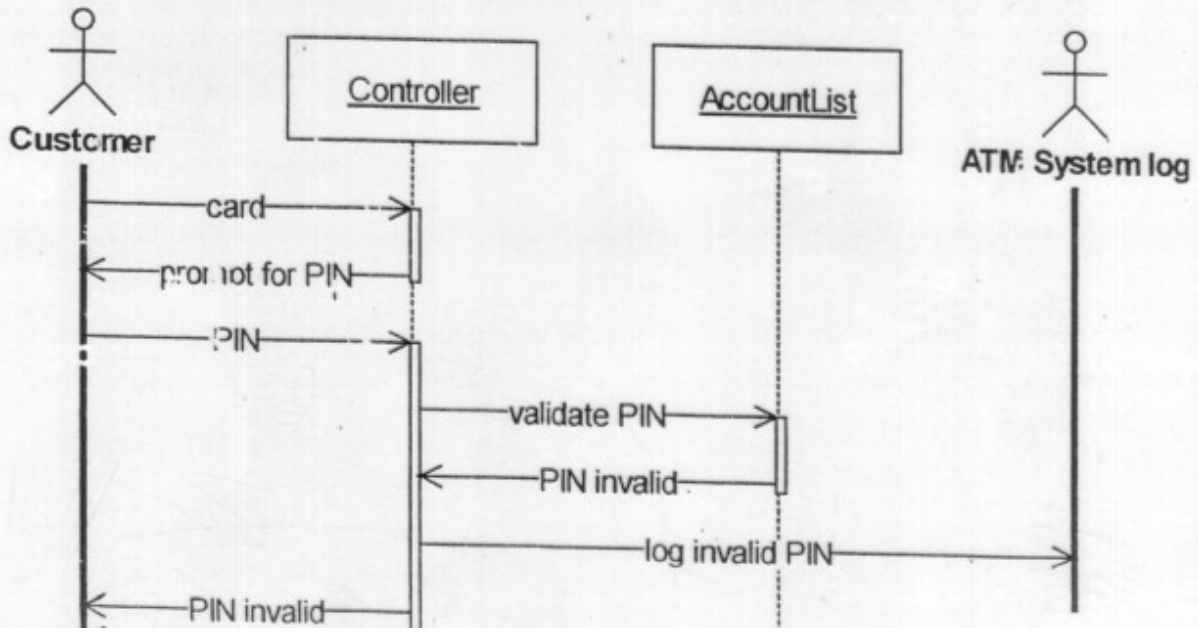
Sequence Diagram Example: Withdraw Cash:Invalid PIN



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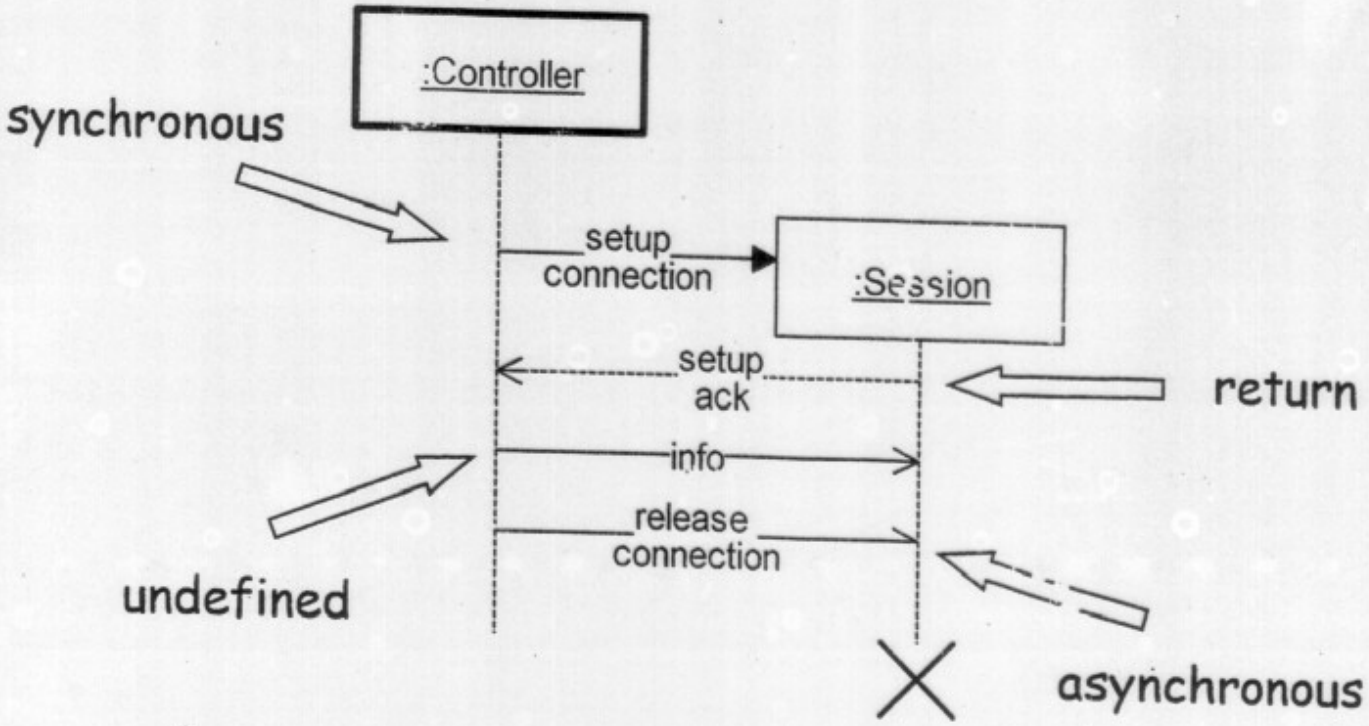
Sequence Diagram (continued)



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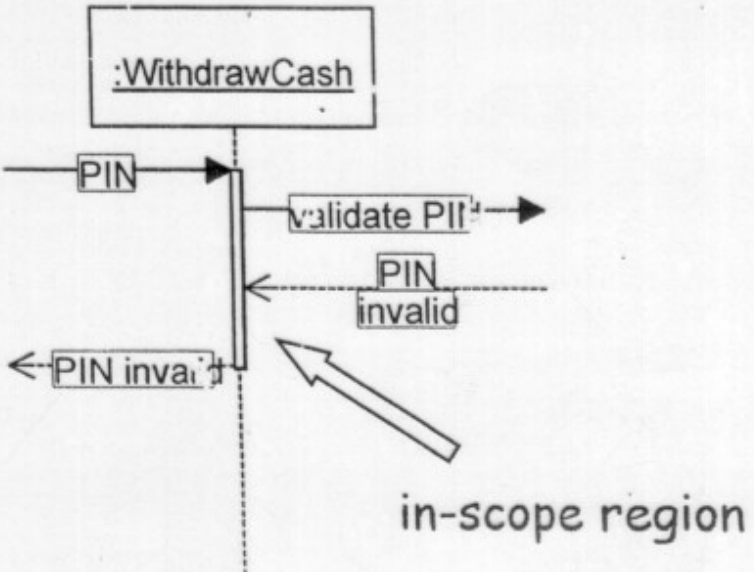
Sequence Diagram (continued)

The period of time when the object is executing and/or waiting for a return message is called in-scope region.



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Build Static Model Using Class Diagrams

- Purpose: define the core classes which comprise the logical structure of the system
- The class diagram is the back-bone of the whole UML model
- Specifies the entities in the system, their attributes and operations and the way they relate to each other.

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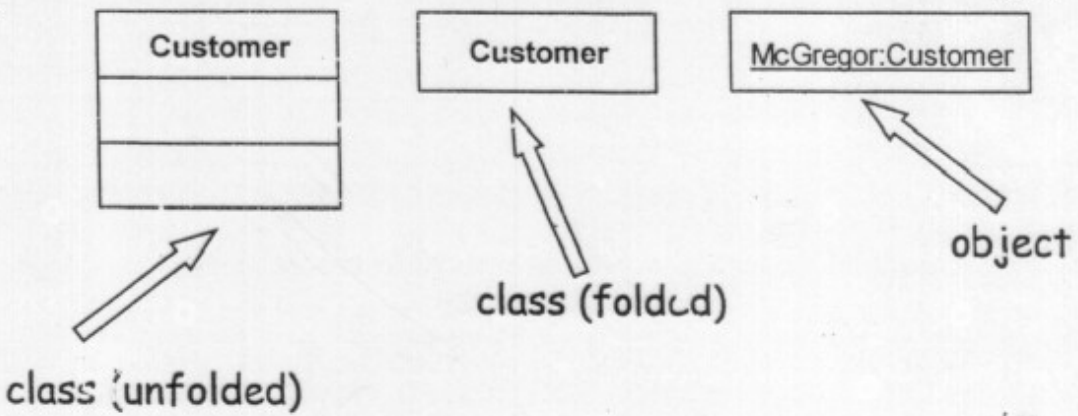
Classes and objects

- A ***class*** is a descriptor for a set of objects that share the same definitions of attributes, operations, and relationships
- An ***object*** is an instantiation of a class



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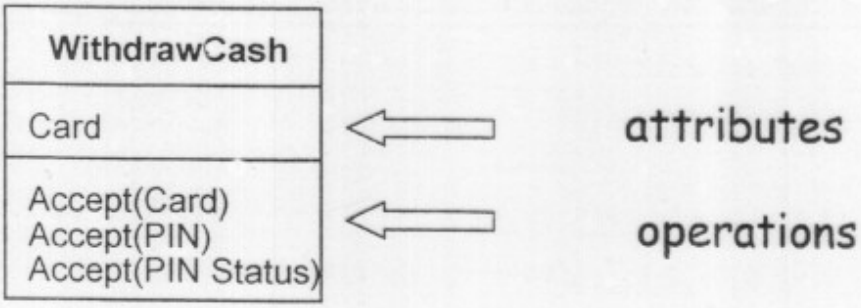
Attributes and operations



- Attributes and operations are refined from messages (and the responsibilities those messages imply) in the classes that receive them.
- Operations will not necessarily coincide with messages.
- Message is an abstract concept, not a property of a class.

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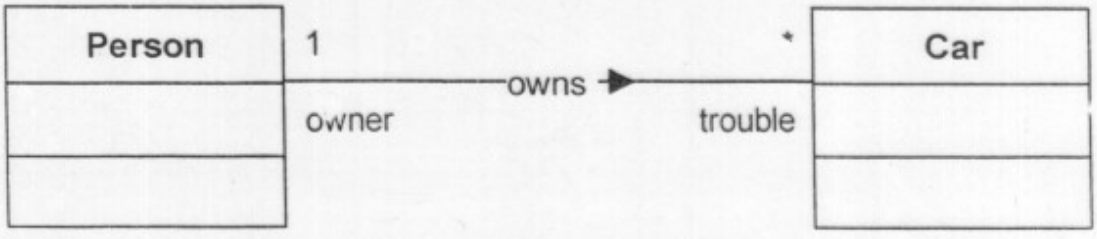
Association

- An association shows that the classes are related with each other in a way that is interesting to model

"one person owns zero to many cars"



"a person is the owner of a car"

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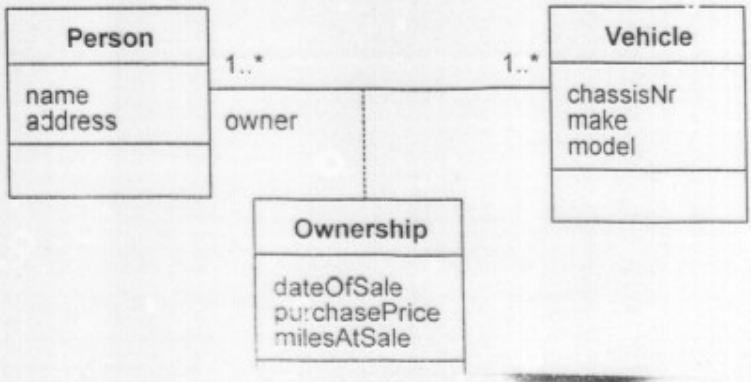
Other elements - Association Classes

- Association classes occur with "many-to-many" associations
- It is rather an association with the characteristics of a class
  - Attributes and operations belong to the link, not to the objects



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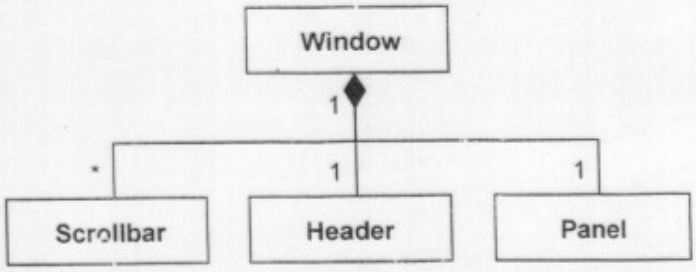
Composition

- A whole is responsible for the creation and destruction of its parts
- A part can belong to only one whole at a time
- When a whole is destroyed, its parts must cease to exist as well
- It's a strong form of aggregation



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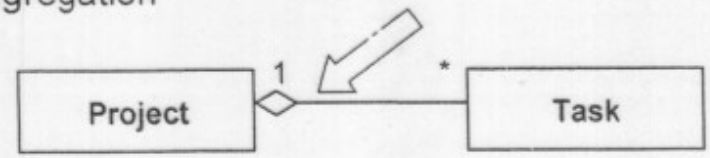
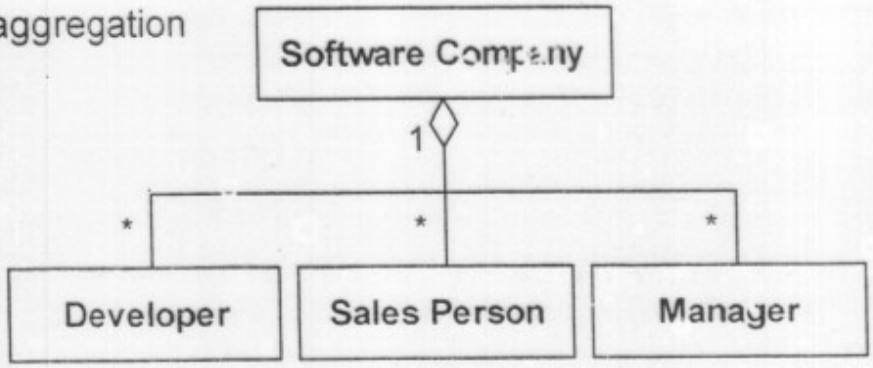
Aggregation

Aggregation symbol

Homogeneous aggregation



Heterogeneous aggregation



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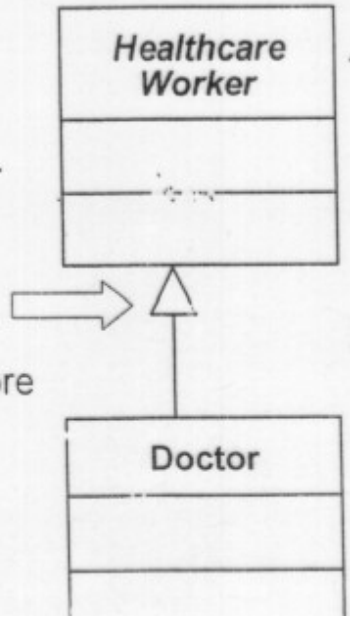
Generalization



Note: italics indicate an abstract class (a class without any direct instances)

Hollow triangular arrowhead and solid line

Arrow points to the more general class

There are instances of "Doctor", but no instances of just "Health Care Worker"

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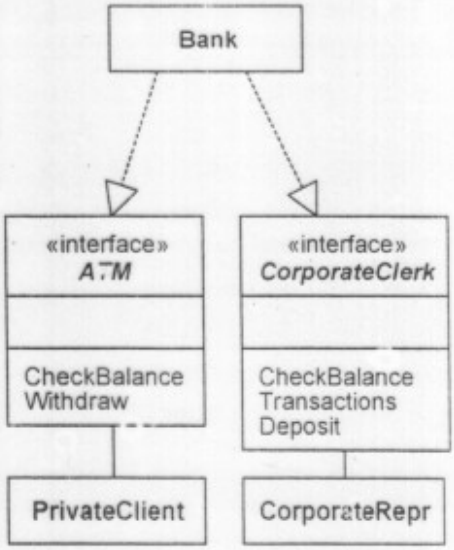
Other elements - Interfaces

- A description of the externally visible and accessible behavior of a class, a package or a component.



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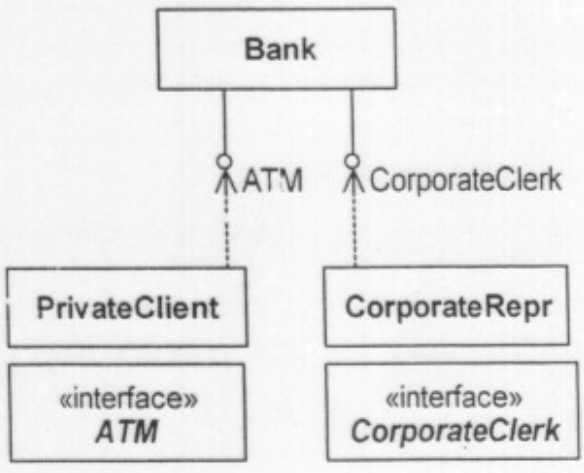
Interfaces - The Interface symbol

- **UML notation:** represented as a small circle attached to the supporting entity by a solid line (a lollipop)
- **Application :** formally equivalent to an interface class. The interface class definition must appear in the class diagram.



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Building Dynamic Model using Statechart Diagrams

- Purpose
  - Specify the life-cycle of an object, the events it responds to, and its behavior in response to those events
- A dynamic model must be created, but statecharts do not have to be used for all parts of it
- In RT and embedded software development, statecharts are often considered to be *the* core technique.

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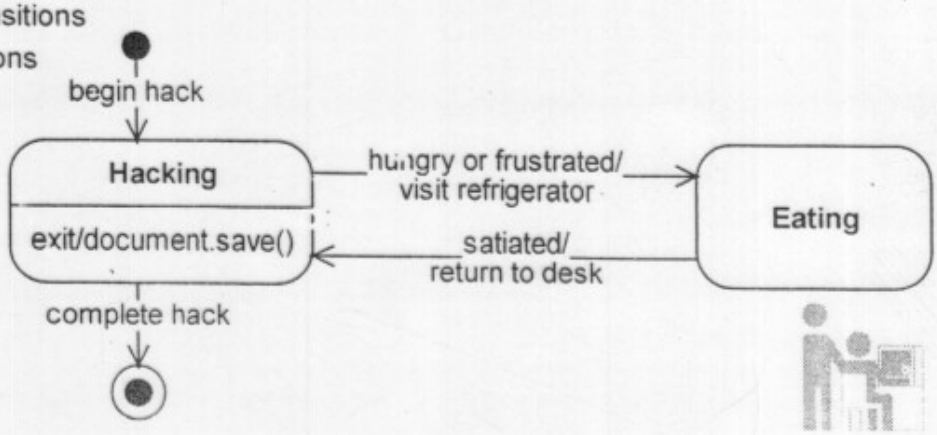
UML statechart diagrams

- A statechart depicts the behavior of an object as
  - States the object can be in.
  - Permissible transitions between states
  - How the object responds to events with
    - Transitions
    - Actions



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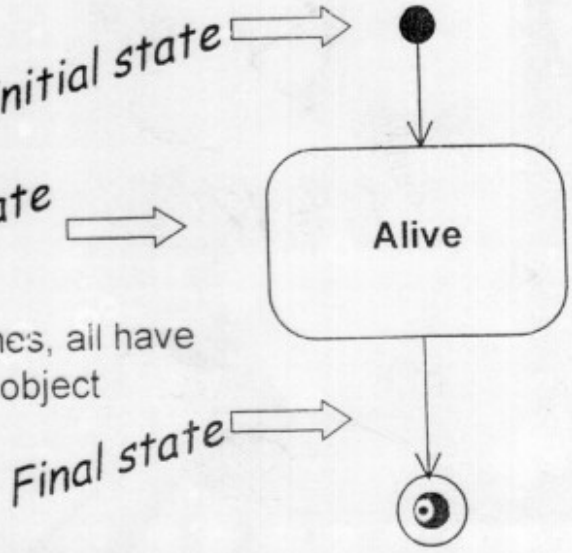
States

A period of time in the life of an object during which it:

- Satisfies some condition, and/or
- Performs some activity, and/or
- Waits for some events

Some special states:

- Initial state: At most one
- Final state: Can appear many times, all have the same meaning - namely, the object ceases to exist



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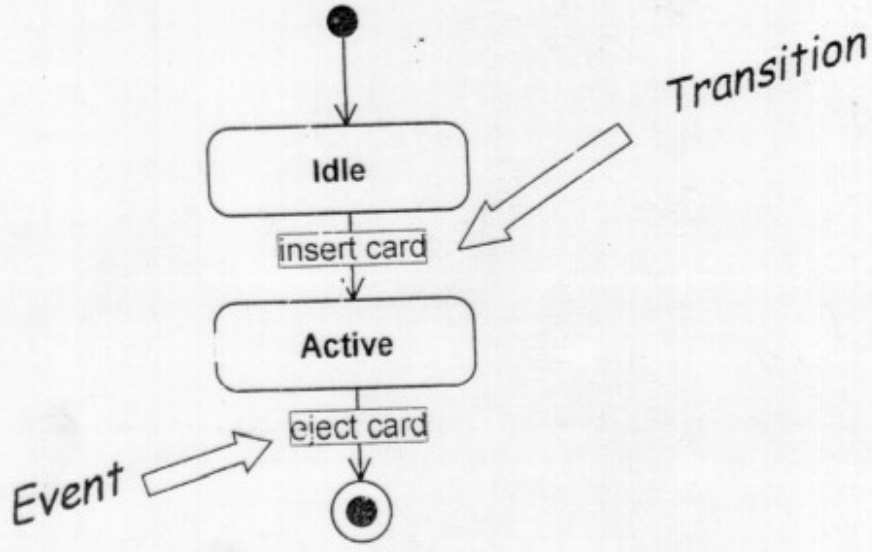
Transitions and Events



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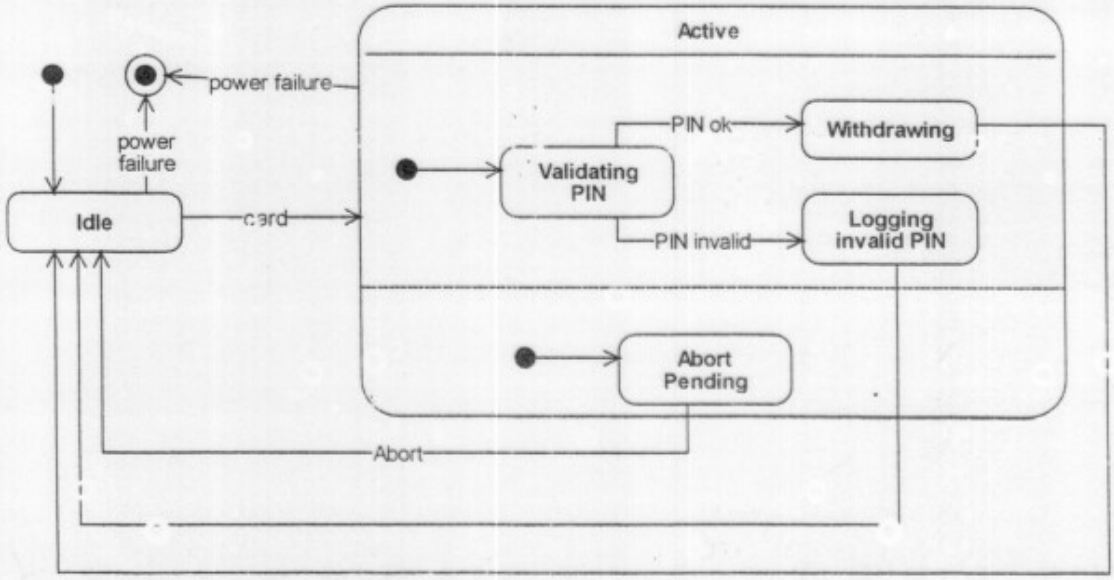
Statechart Example WithdrawCash:Detailed



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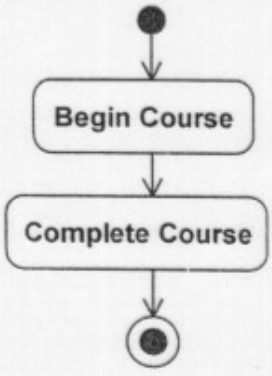
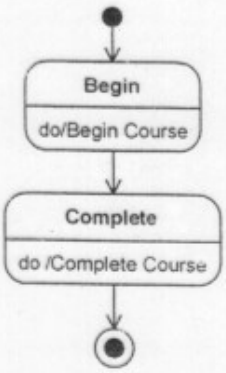
Other Notations - Activity Diagram

- Documenting procedural steps of a single operation
- Emphasizes procedural flow rather than (explicit) event-driven flow





Activity diagram *vs.* *Statechart diagram*

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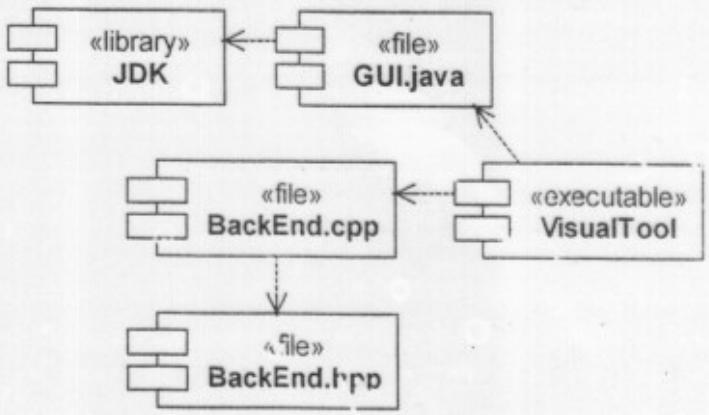
Other Notations - Component Diagram

A component diagram shows the dependencies among software components, including source code components, binary code components and executable components.



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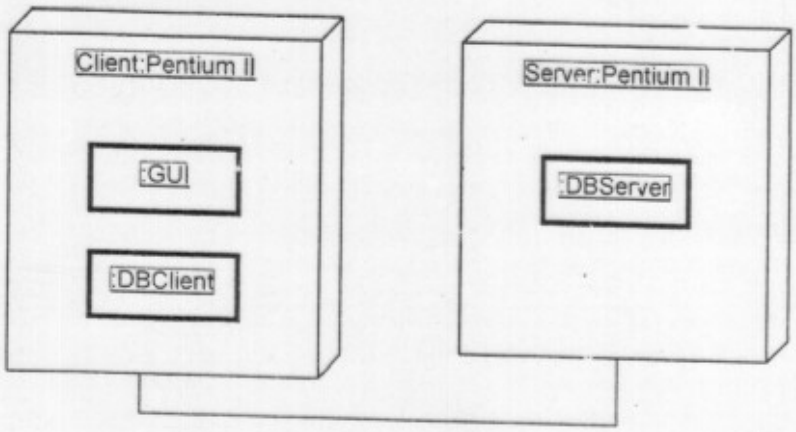
Other notations - Deployment Diagram

- Models how a system will be implemented
- Shows the configuration of run-time processing elements and the software components, processes and objects that live on them



Specifikace tříd

Attributes

Public

Protected

Private

Implementation

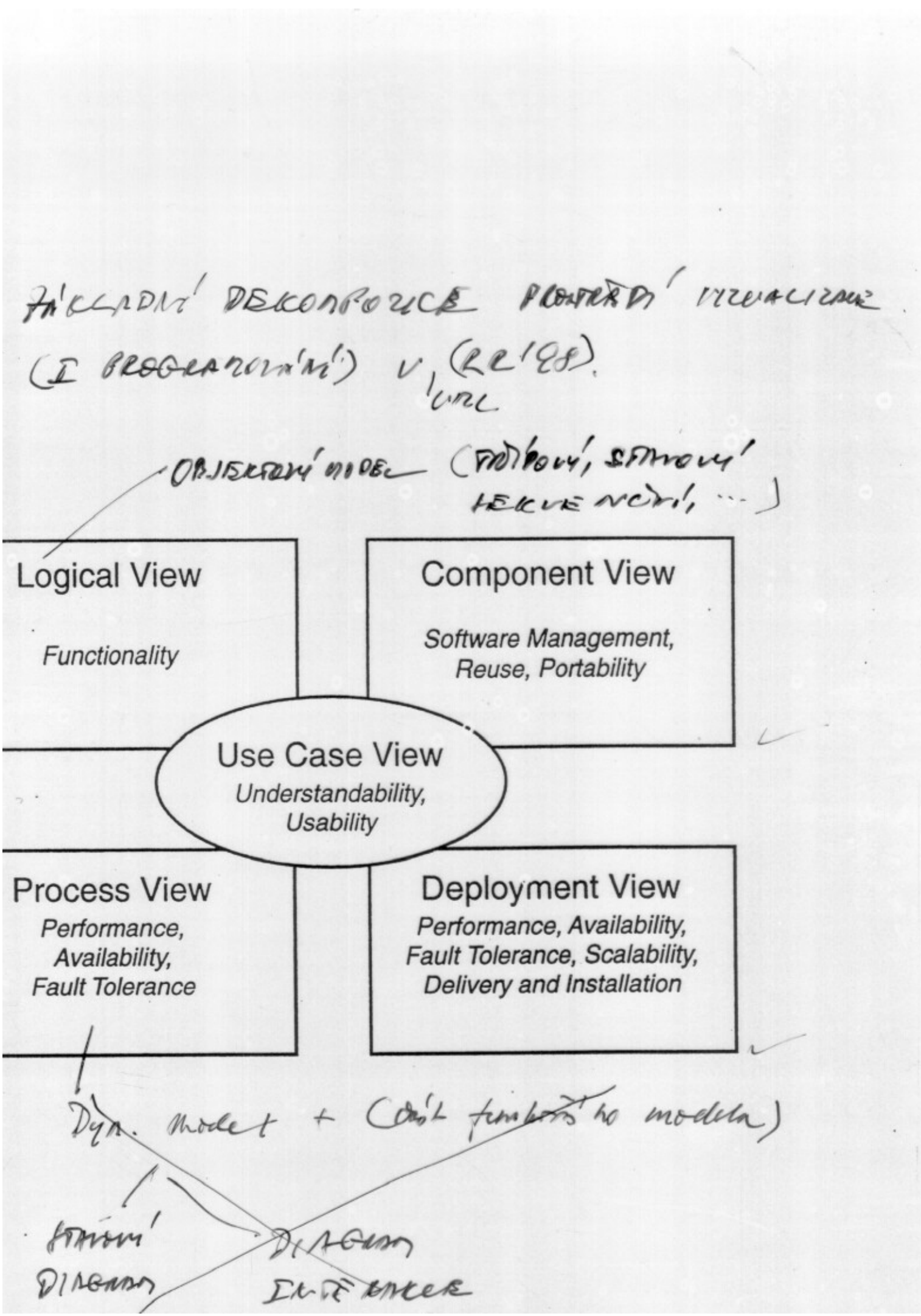
Operations

Public

Protected

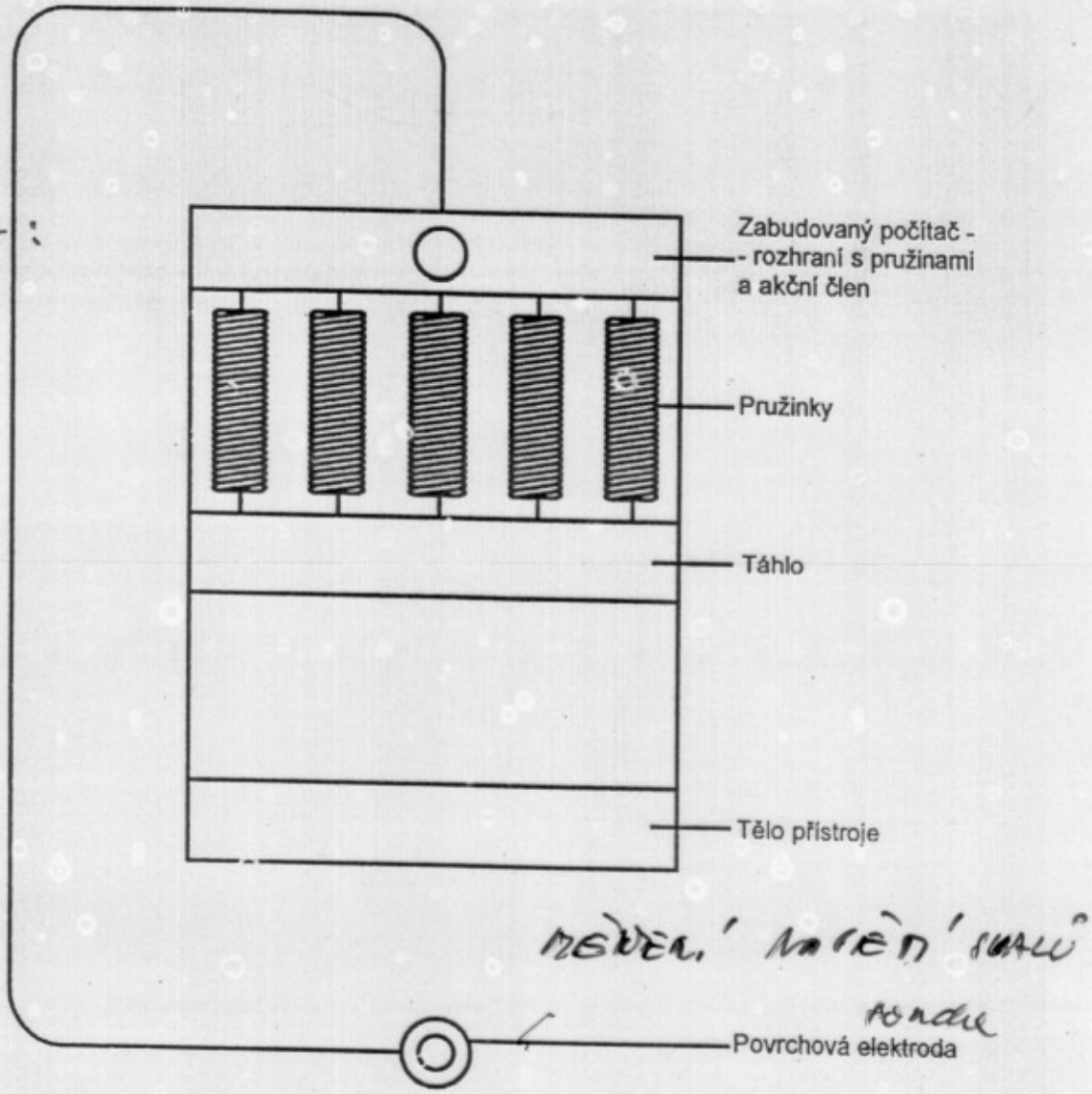
Private

Implementation

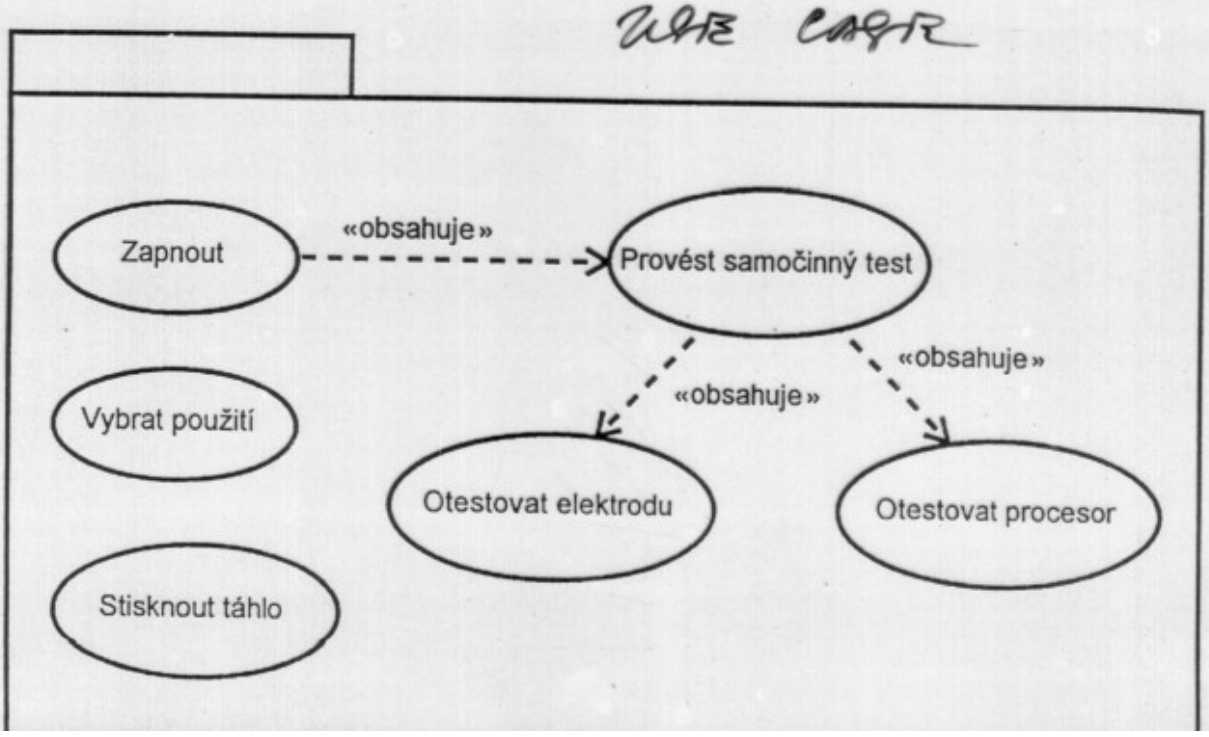

Logical View
Functionality
Component View
Software Management,
Reuse, Portability
Use Case View
Understandability,
Usability
Process View
Performance,
Availability,
Fault Tolerance
Deployment View
Performance, Availability,
Fault Tolerance, Scalability,
Delivery and Installation

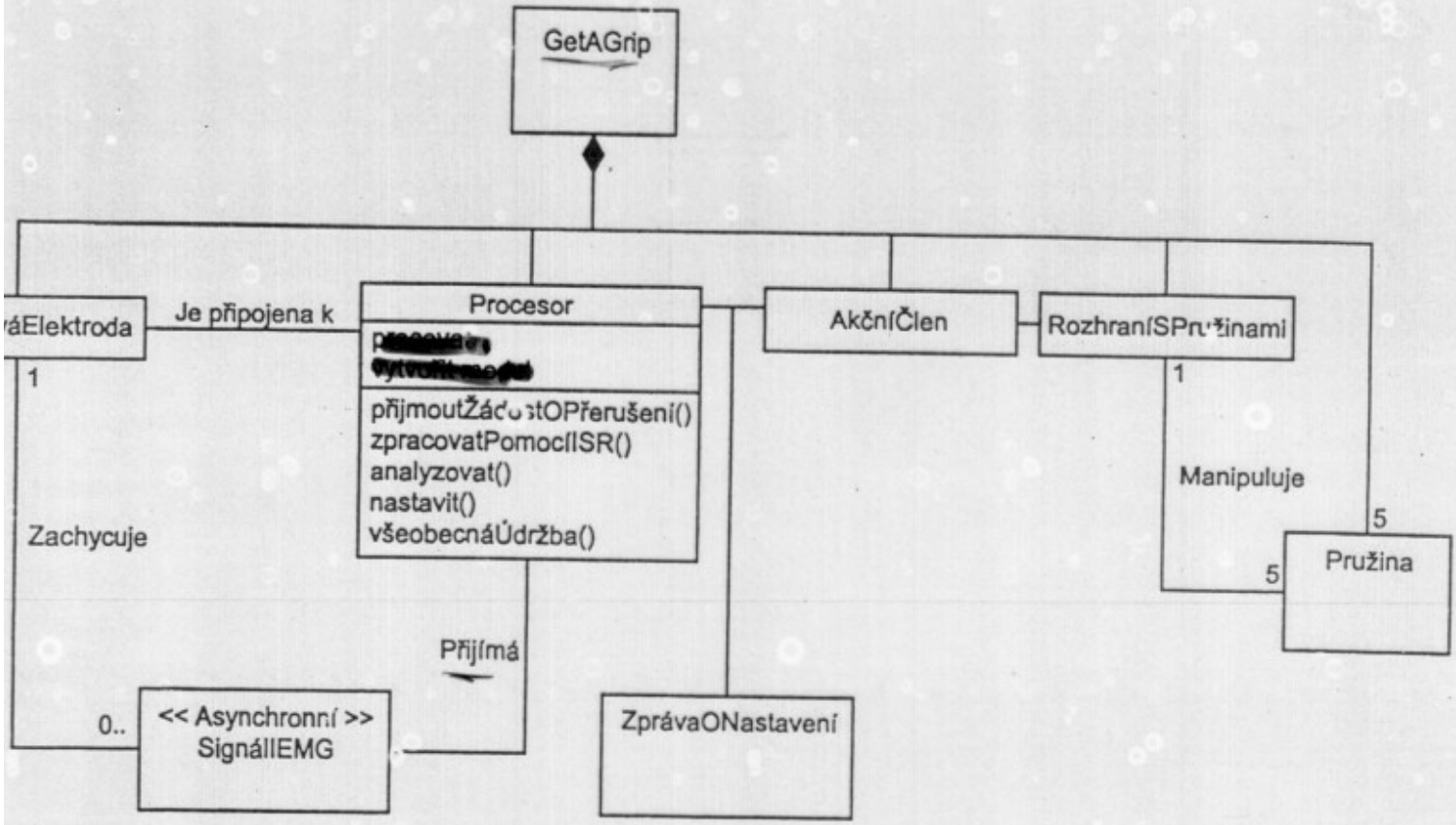
POSILOVAČ SVALŮ DLANĚ

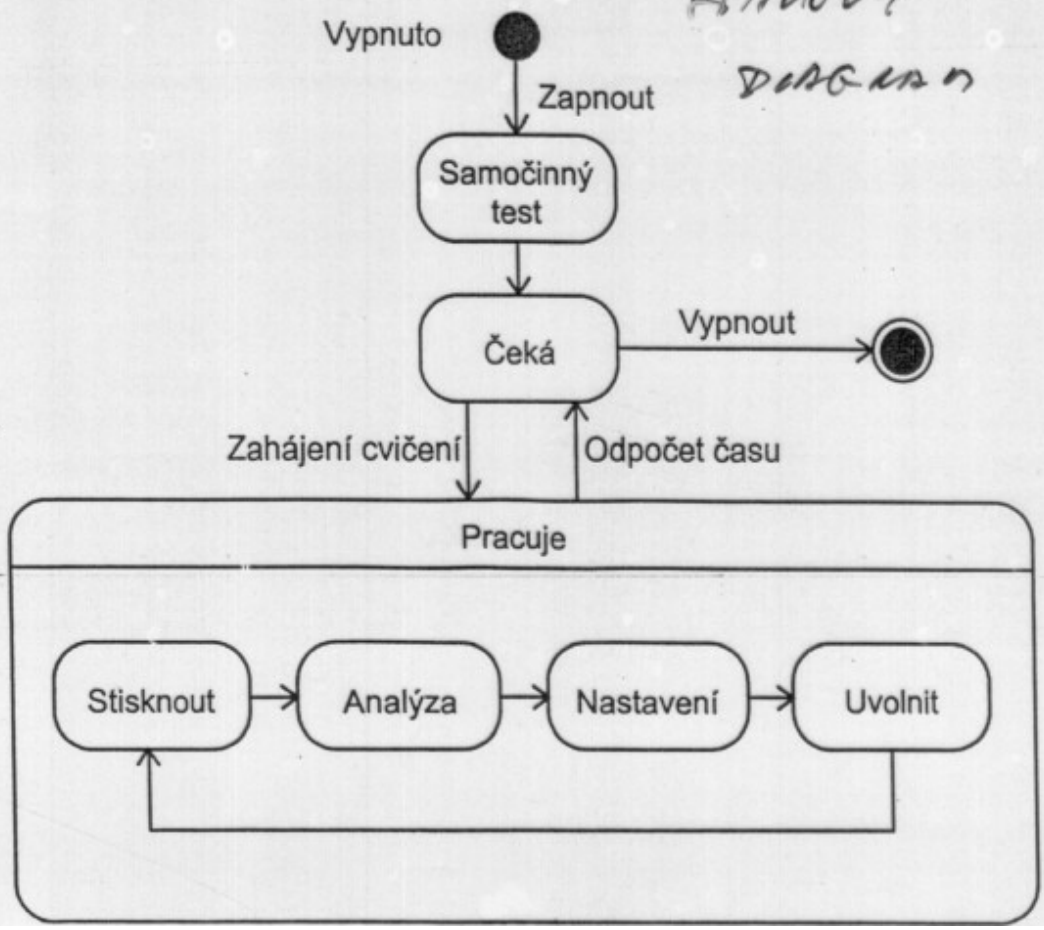
SCHEMA:

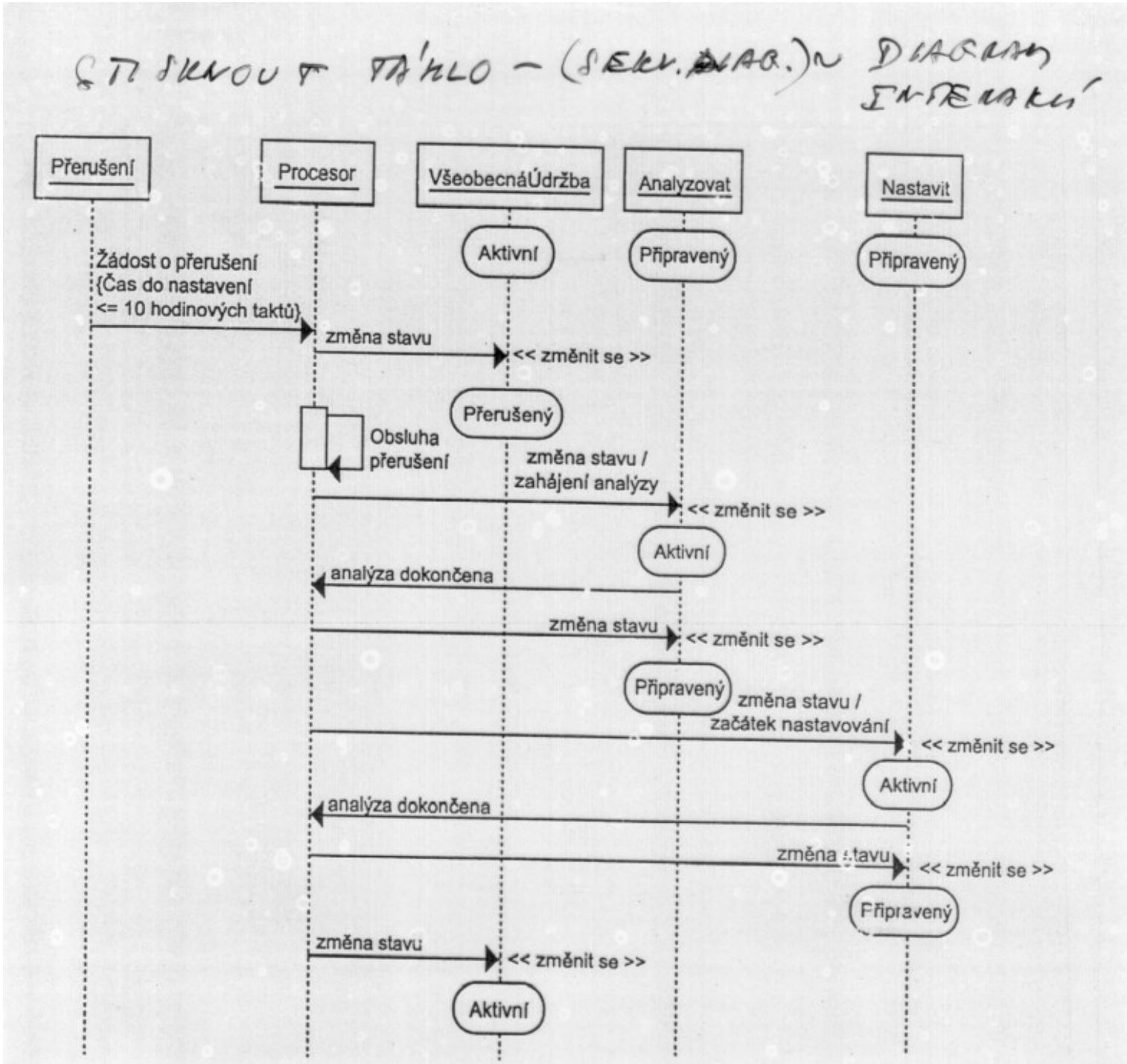


USE CASE




GetAGrip
Procesor
přijmoutŽádostOPřerušení()
zpracovatPomocíISR()
analyzovat()
nastavit()
všeobecnáÚdržba()
AkčníČlen
RozhraníSPružinami
Je připojena k
1
Zachycuje
Manipuluje
5
Pružina
Přijímá
0..
<< Asynchronní >>
SignálEMG
ZprávaONastavení


Vypnuto
Zapnout
Samočinný test
Čeká
Vypnout
Zahájení cvičení
Odpočet času
Pracuje
Stisknout
Analýza
Nastavení
Uvolnit


Přerušení
Procesor
VšeobecnáÚdržba
Analyzovat
Nastavit
Aktivní
Připravený
Připravený
Žádost o přerušení
{Čas do nastavení
<= 10 hodinových taktů}
změna stavu
<< změnit se >>
Přerušený
Obsluha
přerušení
změna stavu /
zahájení analýzy
<< změnit se >>
Aktivní
analýza dokončena
změna stavu
<< změnit se >>
Připravený
změna stavu /
začátek nastavování
<< změnit se >>
Aktivní
analýza dokončena
změna stavu
<< změnit se >>
Připravený
změna stavu
<< změnit se >>
Aktivní

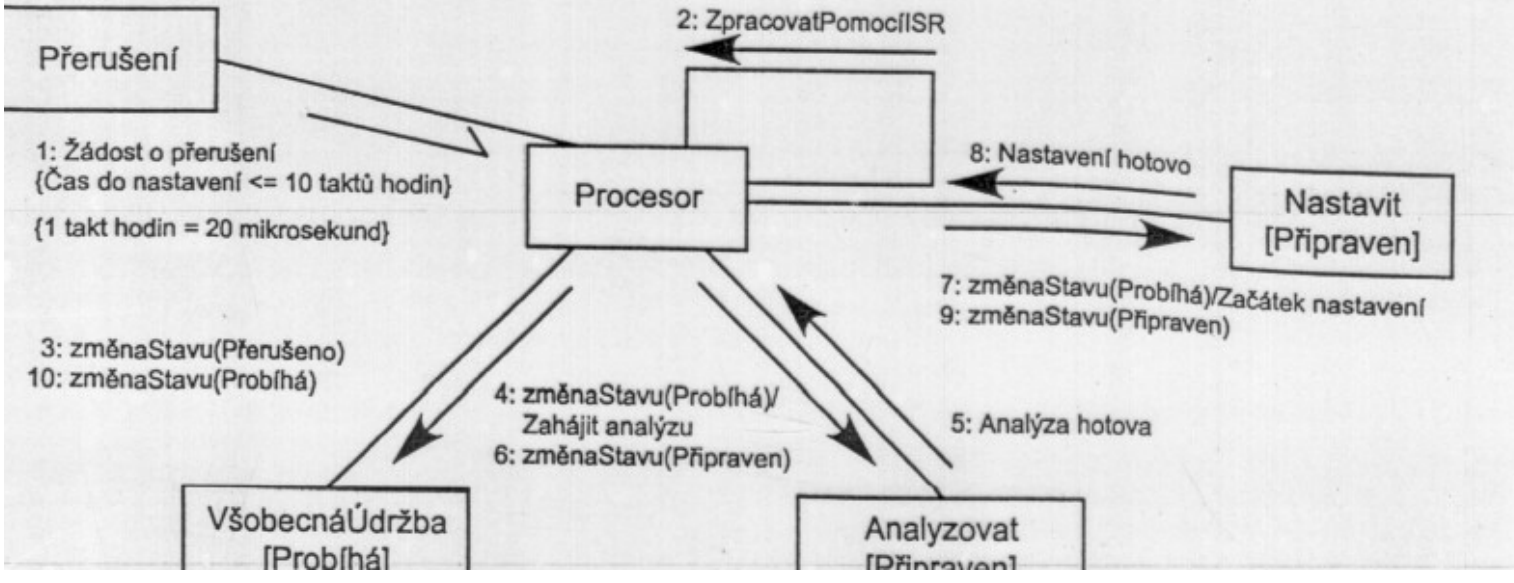

Přerušení
2: ZpracovatPomocíISR
1: Žádost o přerušení
{Čas do nastavení <= 10 taktů hodin}
{1 takt hodin = 20 mikrosekund}
Procesor
8: Nastavení hotovo
Nastavit
[Připraven]
7: změnaStavu(Probíhá)/Začátek nastavení
9: změnaStavu(Připraven)
3: změnaStavu(Přerušeno)
10: změnaStavu(Probíhá)
4: změnaStavu(Probíhá)/
Zahájit analýzu
6: změnaStavu(Připraven)
5: Analýza hotova
VšobecnáÚdržba
[Probíhá]
Analyzovat

DIAGRAM SPOLUPRÁCE

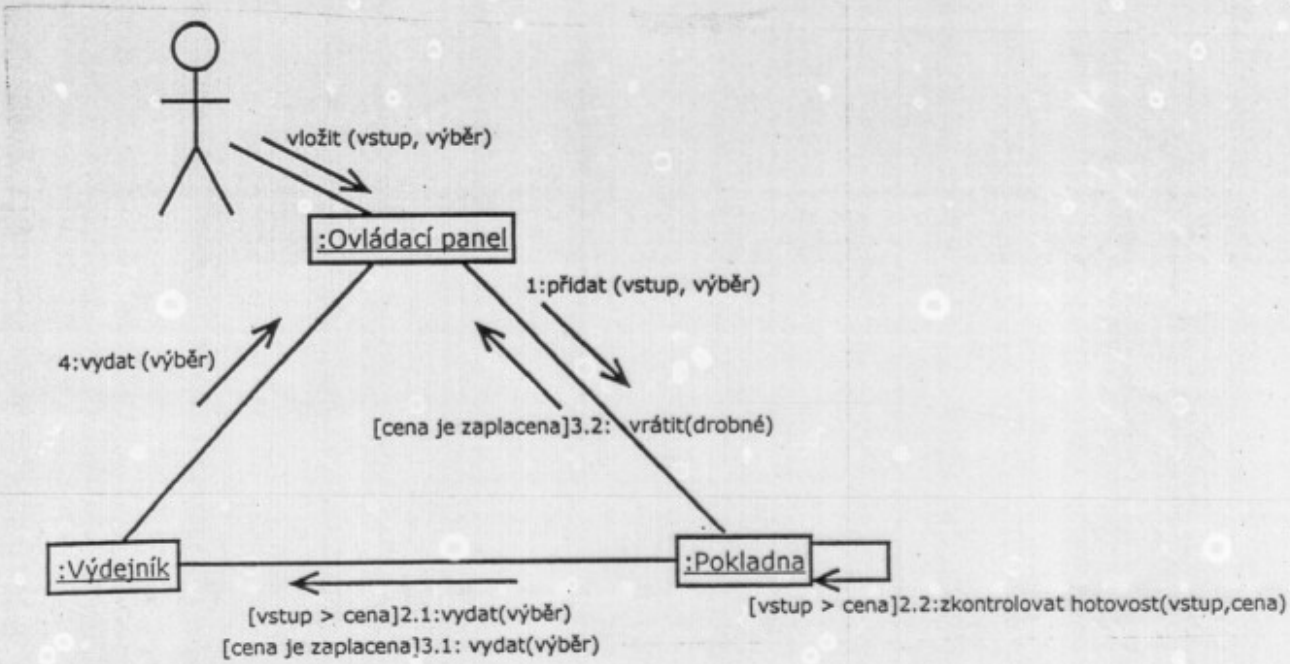


DIAGRAM SPOLUPRÁCE S DETAILY

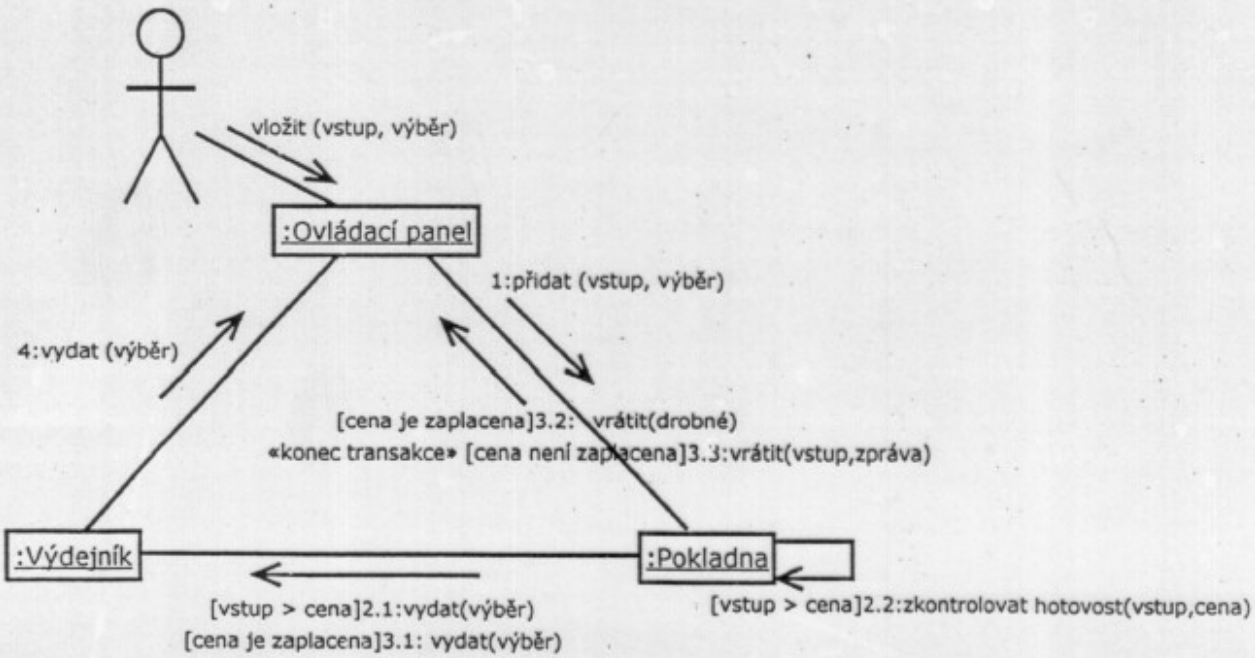


DIAGRAM INTERAKCÍ

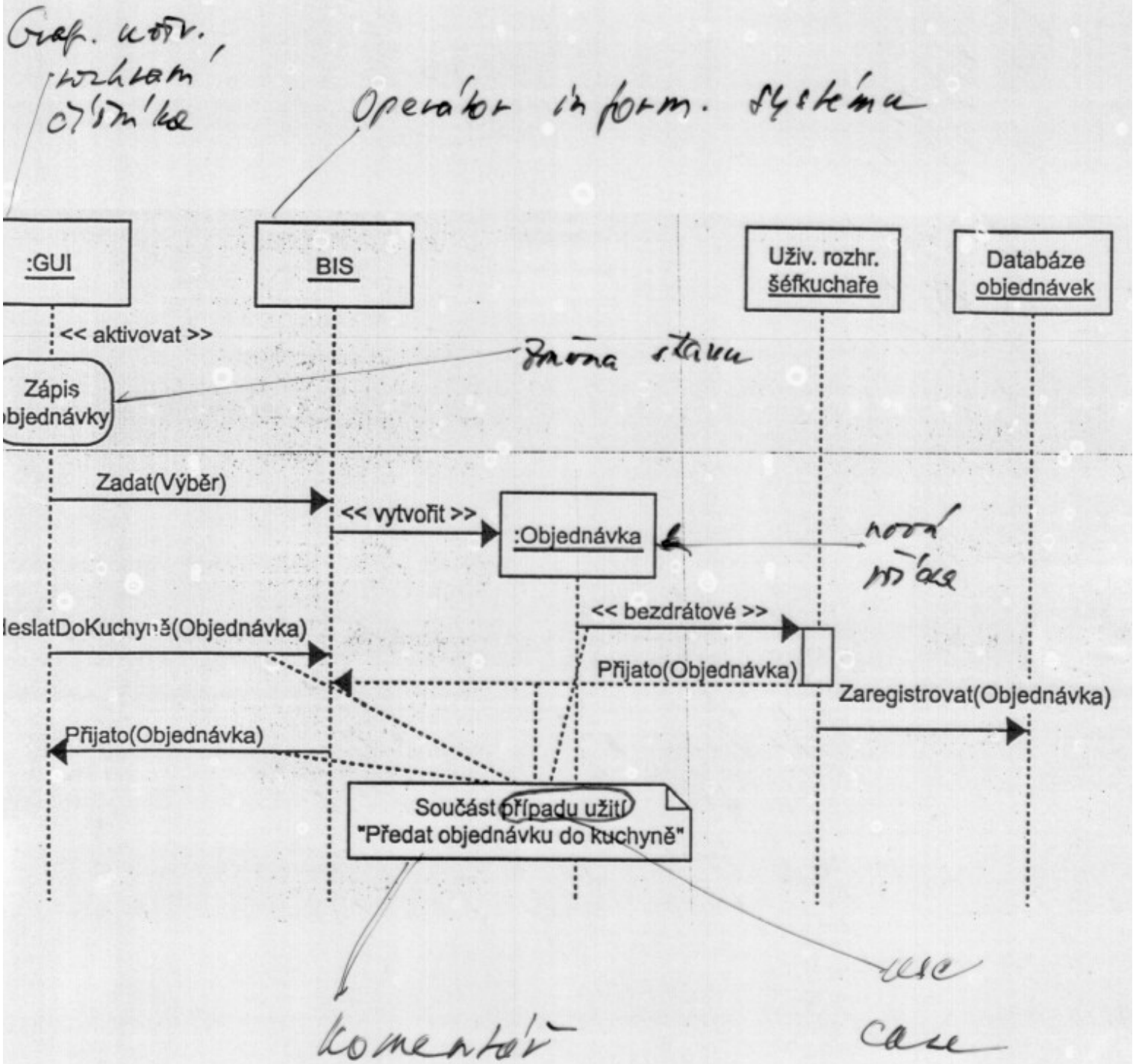


DIAGRAM ČINNOSTÍ

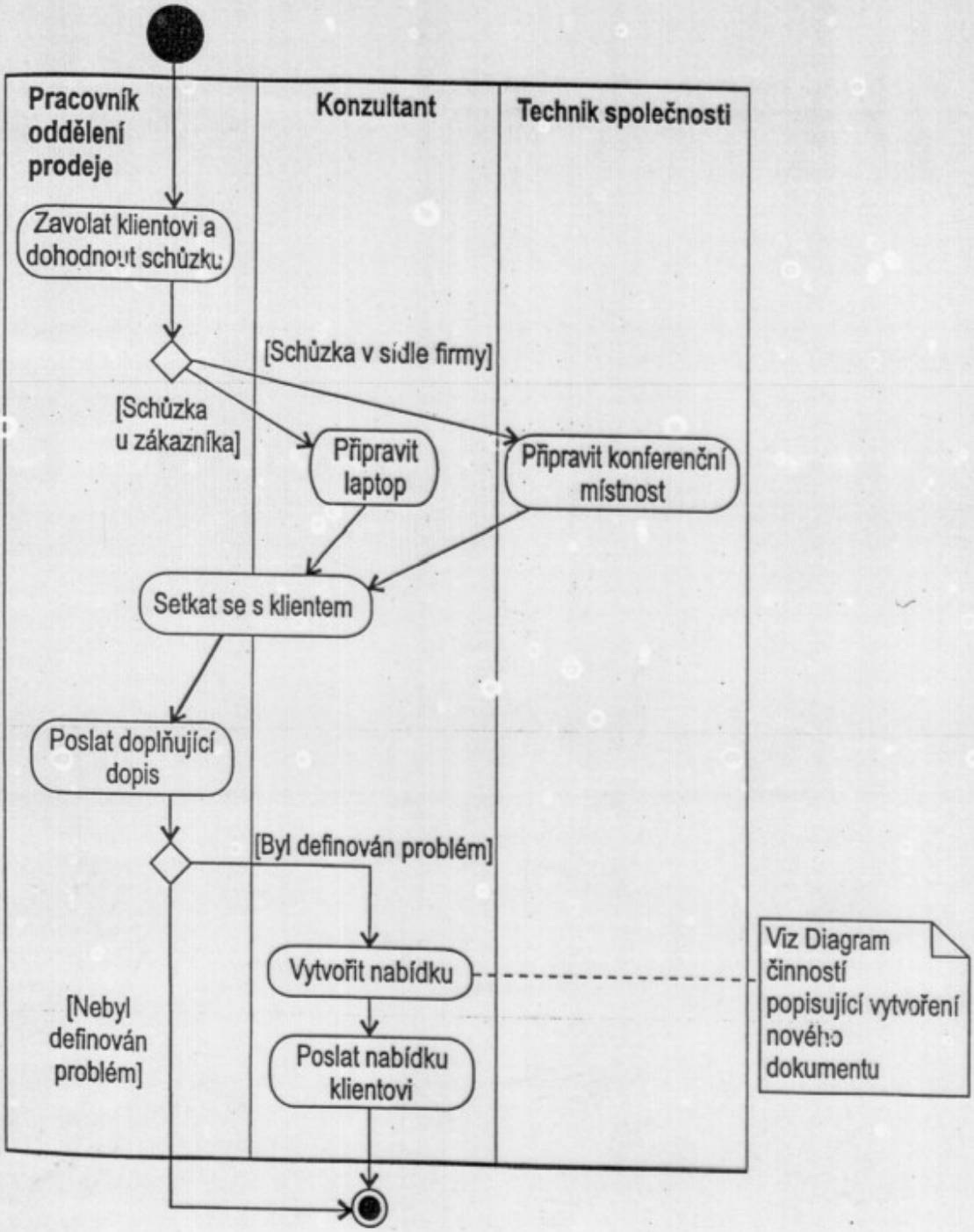
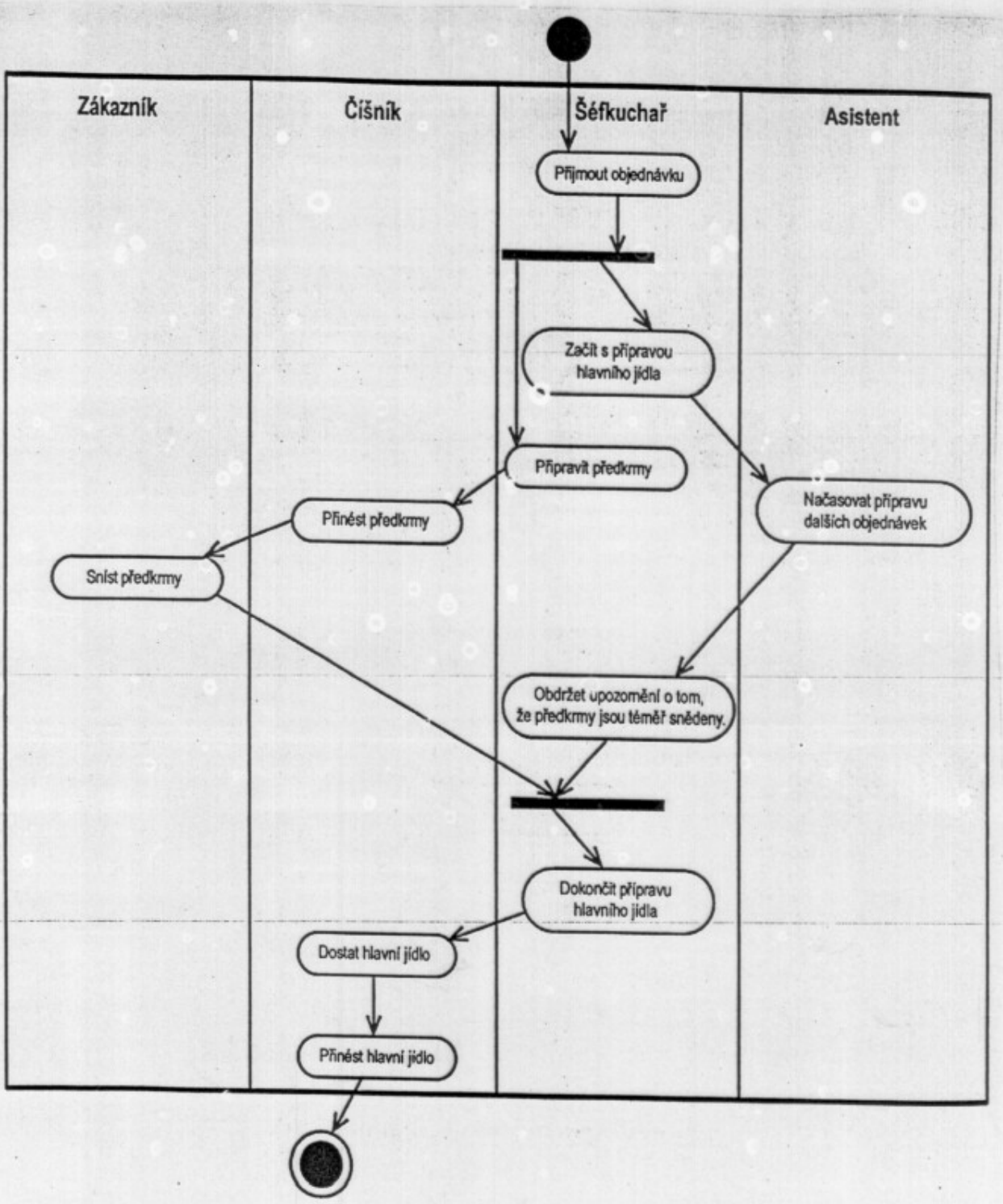
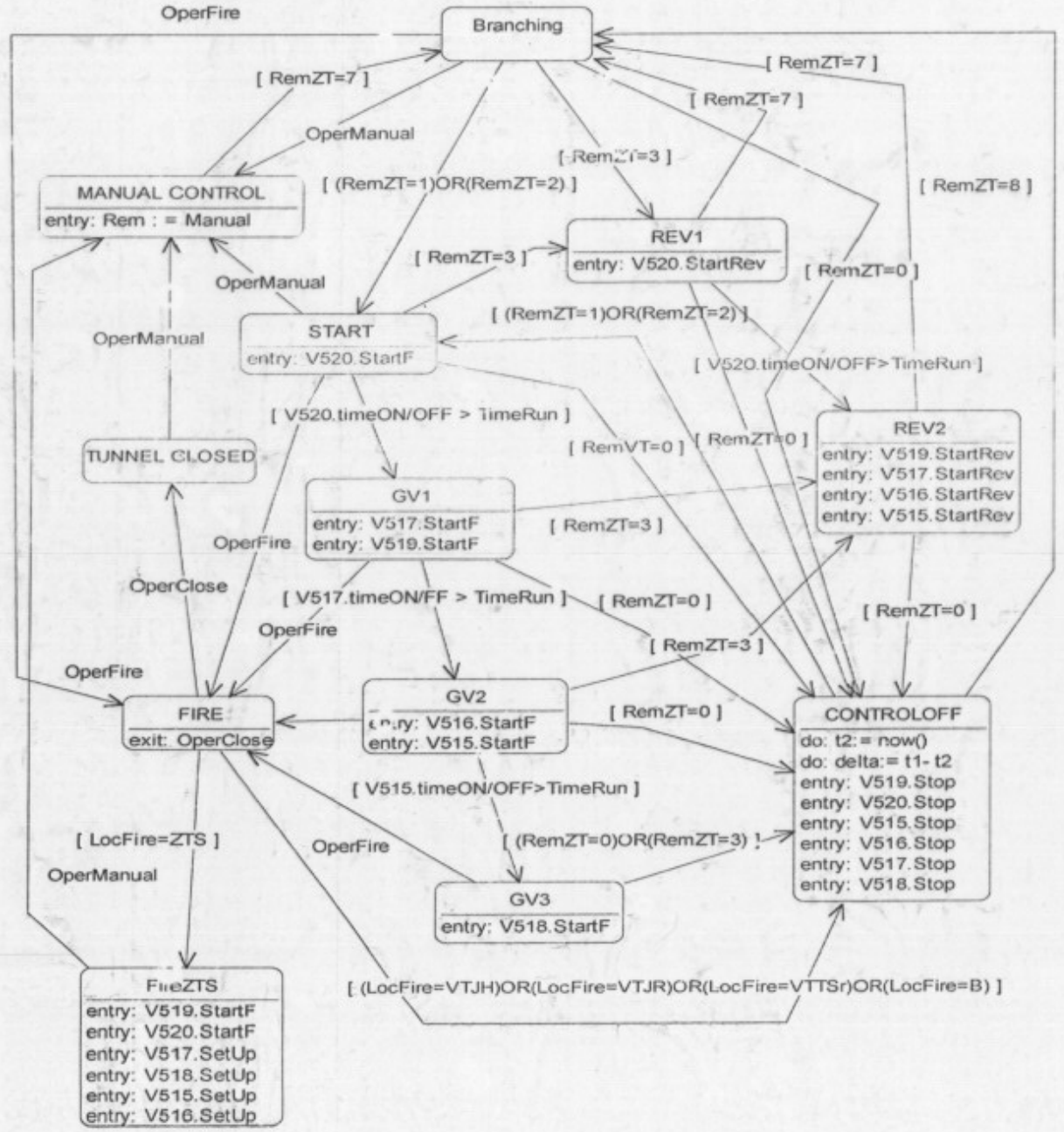


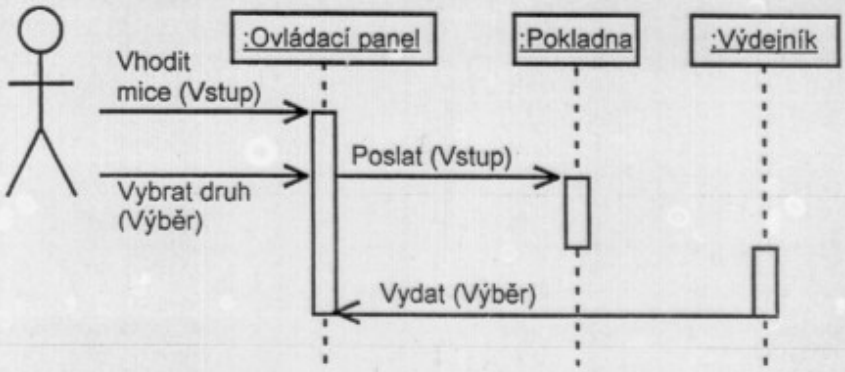
DIAGRAM ROLÍ – DETAIL



State Diagram of the class Tsf6



SEKVENČNÍ DIAGRAM



SEKVENČNÍ DIAGRAM S KETTENÍM

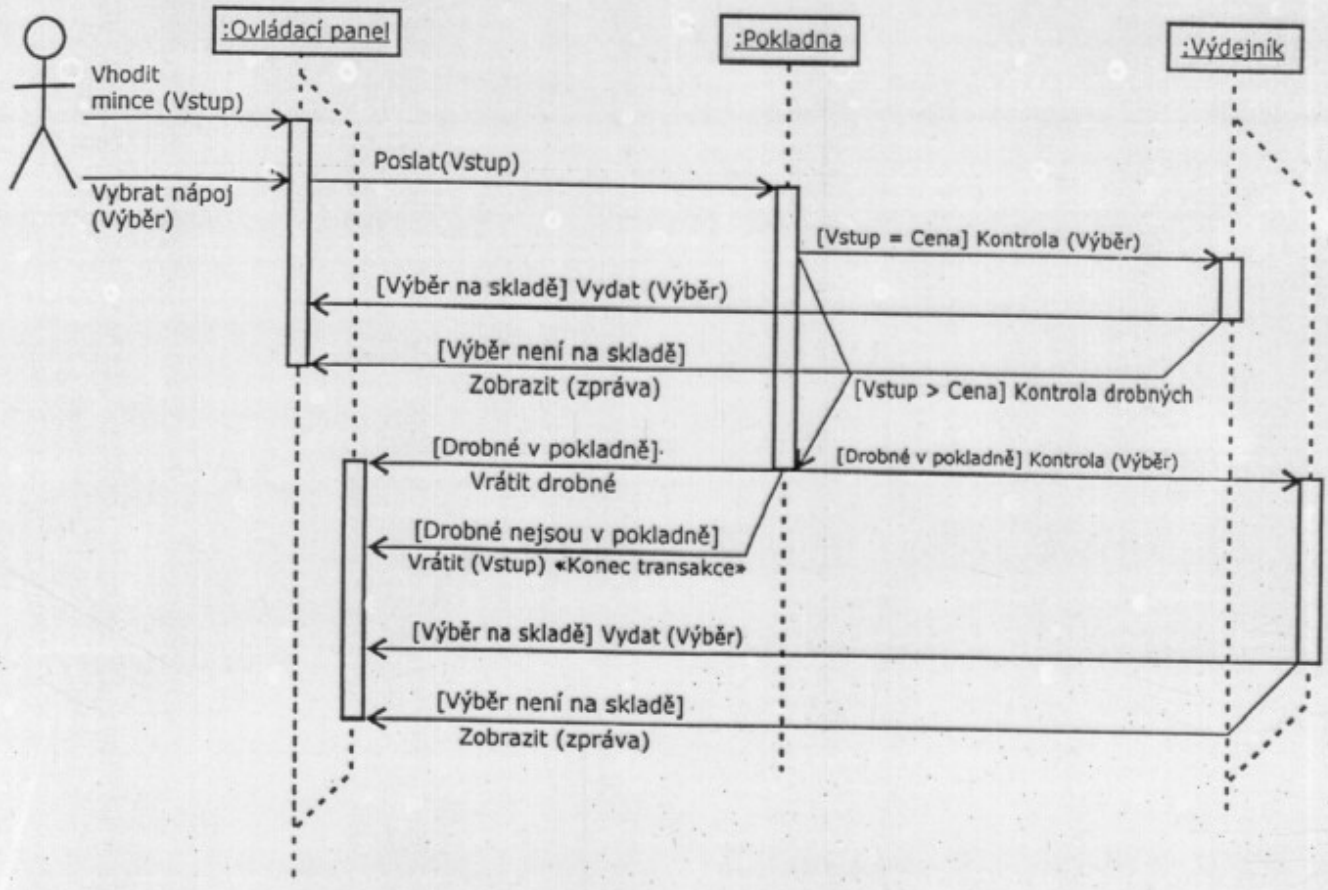
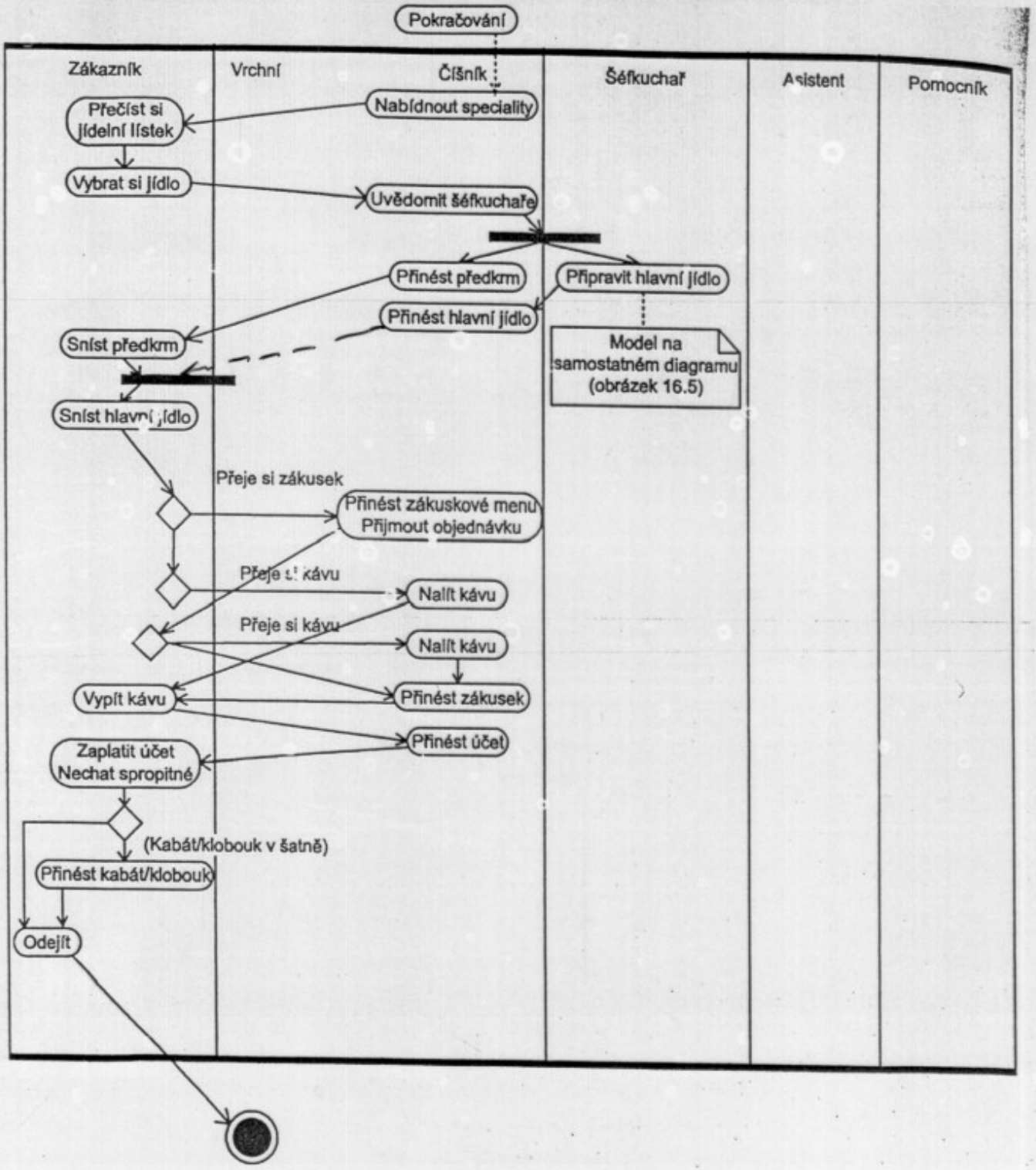
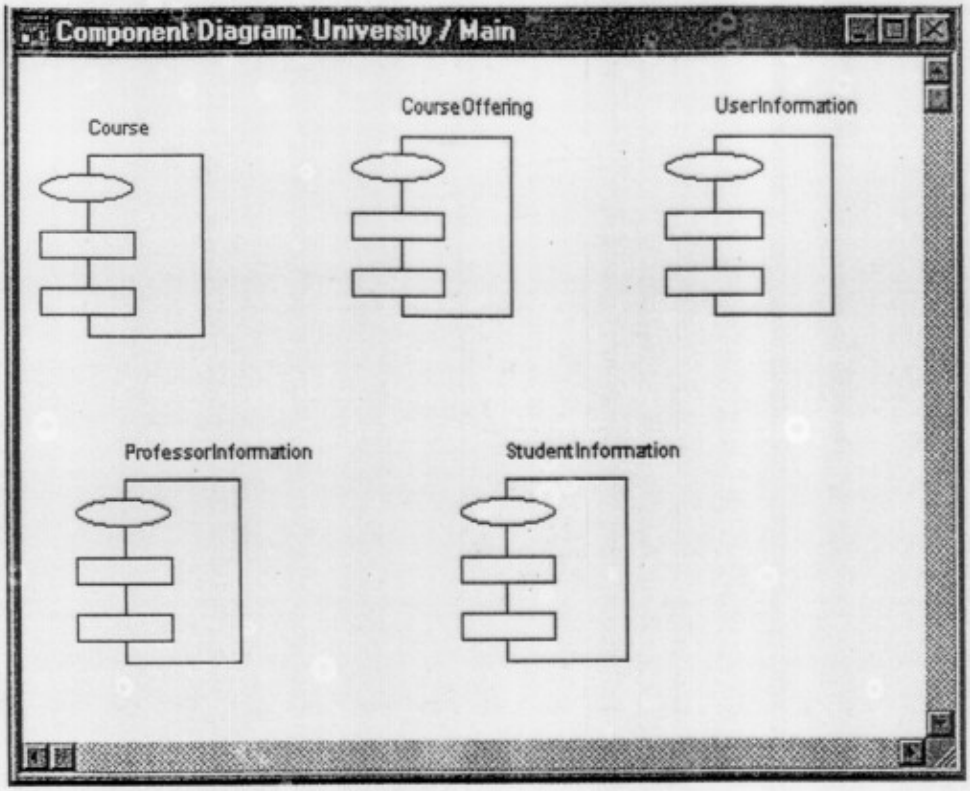
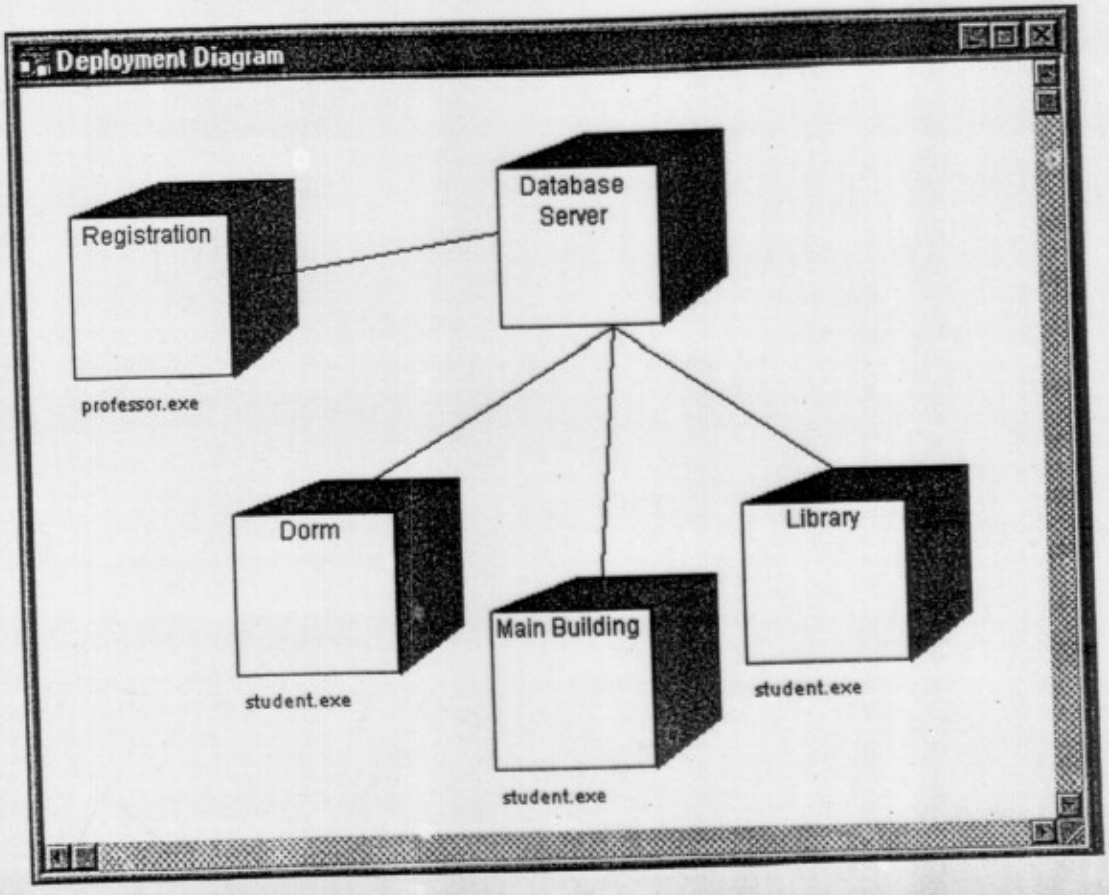


DIAGRAM ROLÍ




Component Diagram: University / Main
Course
CourseOffering
UserInformation
ProfessorInformation
StudentInformation


Deployment Diagram
Database Server
Registration
professor.exe
Dorm
student.exe
Main Building
student.exe
Library
student.exe